FORM PT01449 (REV. 8-83)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. HARI.006USP	SERIAL NO. 09/103,056
INFORMATION DISCLOSURE STATEMENT *(Use several sheets if necessary)*		APPLICANT ELIYAHOU HARARI et al.	
		FILED June 23, 1998	GROUP 2785 2878

U. S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUB CLASS	FILING DATE
vN	A1	3	6	3	3	1	7	5	1/4/72	Harper			
vN	A2	4	0	5	1	3	5	4	9/27/77	Choate			
vN	A3	4	2	5	0	5	7	0	2/10/81	Tsang et al.			
vN	A4	4	2	8	1	3	9	8	7/28/81	McKenny et al.			
vN	A5	4	3	5	4	2	5	3	10/12/82	Naden			
vN	A6	4	3	5	5	3	7	6	10/19/82	Gould			
vN	A7	4	3	8	0	0	6	6	4/12/83	Spencer et al.			
vN	A8	4	4	0	5	9	5	2	9/20/83	Slakmon			
vN	A9	4	4	2	2	1	6	1	12/20/83	Kressel et al.			
vN	A10	4	4	5	0	5	5	9	5/22/84	Bond et al.			
vN	A11	4	4	6	3	4	5	0	7/31/84	Haeusele			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY		CLASS	SUB CLASS	TRANS.? (YES/NO)
vN	B1	AU	0	5	5	7	7	2	3	~~1987~~	Australia	1987.01.08			
vN	B2	DE	2	8	4	0	3	0	5	~~1980~~	Germany	1980.03.27			Abstract
vN	B3	EP	0	0	8	6	8	8	6	~~1983~~	EPO	1983.08.31			
vN	B4	EP	0	2	2	0	7	1	8	~~1987~~	EPO	1987.05.06			
vN	B5	EP	0	3	0	0	2	6	4	~~1989~~	EPO	1989.01.25			

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10.31.06

·OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent pages, Etc.)*

vN	C1	Jex "Flash Memory BIOS for PC and Notebook Computers", 1991, IEEE, pp. 692-695.
vN	C2	Wilson, "1-Mbit Flash Memories Seek their role in System Design", Computer Design, vol. 28, no. 5, pp. 30 and 32, March 1989.
vN	C3	Bonner, "Flash Memory for Top Speeds in Mobile Computing Applications", Computer Technology Review, vol. 12, no. 7, June 1992, pp. 36-37.

EXAMINER V. Nguyen	DATE CONSIDERED 1/10/99

* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.